

REMARKS

Claims 1-5, 8-13, 15-26 and 32-45 are pending.

Claims 1-5, 15-26 and 33-45 stand withdrawn from consideration at this time as directed to unelected species.

Claims 8-13 and 32 stand rejected at last examination.

I. Rejections under 35 U.S.C. § 102(e)

Claims 8-10, 12, and 32 stand rejected under 35 U.S.C. 102(e) as being anticipated by Singh et al. *patent No. US 6162697* issued to Singh (herein "Singh").

Those rejections are traversed.

As to claim 8, the rejection under 35 U.S.C. § 102(e) of claim 8 as being anticipated by Singh is traversed on the grounds that Singh does not teach, suggest or render obvious all the limitations of claim 8.

For example, claim 8 recites, in part "...a bondwire having a specified self-inductance and electrically connecting the output port to an external conductor; ...". The Examiner wrote "*... Claims 8 ... rejected ... anticipated by Singh et al. ... Singh (Figs. 4-6) teaches an amplifier integrated circuit ... having an output port on the chip connected to a bondwire (e.g. 62) that is connected to an external conductor (e.g. 3);*".

It is respectfully submitted that the bondwire 62 of Singh does not connect to an output port. The only references in Singh to bondwire 62 and to structures connected thereto are in Singh Column 3 lines 48-51 and Singh figure 6. Those references clearly

indicate connection to pad 61 which serves to electrically connect multiple bondwires but it is not taught as pad 61 or bondwire 62 connecting any on-chip semiconductor structures.

Neither does Singh teach any output ports of any type connected to bondwires.

Moreover, claim 8 recites, in part "... a capacitor having a specified capacitance formed on the semiconductor die and electrically connected between the output port and a ground, wherein: the bondwire and the capacitor are operable to match the output impedance to a desired load impedance.". The Examiner wrote "*... Claims 8 ... rejected ... anticipated by Singh et al. ... Singh (Figs. 4-6) teaches an amplifier integrated circuit ... capacitor (CB2 and C6 in fig. 5) is connected between the output port and ground...*". But in Singh, CB2 is not connected to an output port (Singh Fig. 5) and also, since it is expressly called out to function as a bypass capacitor (Singh column 4 line 51), it cannot be operable to match an output impedance. Furthermore, Singh does not teach that C6 is an *on-chip* capacitor.

Thus, Singh does not teach all the limitations of claim 8 and claim 8 should be allowable under 35 U.S.C. § 102(e) over Singh.

As regards claims 9, 10 and 12, they are dependent on claim 8 and so are allowable under 35 U.S.C. § 102(e) over Singh for at least the same reasons as claim 8.

As regards claim 32, the examiner has advanced the same arguments as for claim 8 and so claim 32 is allowable under 35 U.S.C. § 102(e) over Singh for at least the same reasons as claim 8

II. Rejections under 35 U.S.C. § 103(a)

Claims 11 and 13 stand rejected under 35 U.S.C. 103(a) as being unpatentable over (rendered obvious by) Singh in view of US Patent 6,256,482 issued to Raab et al..

It is respectfully submitted that claims 11 and 13 are dependent on claim 8 and so are allowable for at least the same reasons as claim 8.

SUMMARY

Claims 8-13 and 32 stand rejected at last examination.

No claims have been amended by this paper..

It is respectfully requested that the Examiner reconsider and allow the rejected claims for the reasons stated and pass this case to issue with pending claims 8-13 and 32 allowed.

The examiner is invited to call the undersigned at (650) 726 3841 if desired to help advance prosecution.

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N.R.H. Black	5/14/2004
Agent	Date of Signature
63,320	

Respectfully submitted,



N.R.H.Black
Agent
Reg. No. 43,320

420 Miramontes Ave.
Half Moon Bay
CA 94019

H.Black P.E.
420 Miramontes Ave.
HALF MOON BAY, CA
94109
(650) 726-3901
FAX (650) 726-9245